



# 27<sup>th</sup> International Symposium on VLSI Design and Test (VDAT-2023)

Sept. 29 - Oct. 01, 2023



SYMPOSIUM VENUE → BITS Pilani - Pilani Campus (Rajasthan) 333 031, INDIA

## VDAT-2023

VLSI Design & Test Symposium (VDAT) promotes research and development in various fields of VLSI Design. VDAT began as a small workshop in the year 1998, and in 2005 it acquired the status of an international symposium. The 27th International Symposium on VLSI Design and Test (VDAT-2023) will be jointly hosted by Birla Institute of Technology & Science (BITS), Pilani, and CSIR - Central Electronics Engineering Research Institute (CSIR-CEERI), Pilani in the month of September at Pilani, Rajasthan, India. This symposium aims to bring Industries, Academics, Researchers, Startups, MSMEs, and related practitioners together to exchange their ideas for leveraging in their respective fields. The VLSI Society of India, leading institutes and industries actively support the symposium.

## SPONSORSHIP OPPORTUNITIES

It gives us real pleasure to present you with an opportunity to contribute and participate as one of the sponsors of the 27<sup>th</sup> International Symposium on VLSI Design and Test (VDAT-2023) to be held in an offline mode at BITS Pilani, Pilani Campus and CSIR-CEERI, Pilani during September 29 - October 1, 2023. The conference will provide a platform to academicians, researchers, engineers who are actively engaged in the field of VLSI design to share their experiences, research results, innovative ideas and discuss the practical challenges encountered and the solutions adopted.

## CONFERENCE SPONSORS

The sponsorship would help in generating financial resources for successfully organizing the conference to meet expenditure on the venue and other logistics. Various types of sponsorships under this category are the following:

Sponsorship Category			Access/ Benefits
Platinum (Rs 500,000/-)	Gold (Rs 400,000/-)	Silver (Rs 300,000/-)	
√	√	√	Product Exhibition Booth
√	√	√	Company's Name and Logo on Conference Website
√	√	√	Banner at Institute Main Entrance Gate
√	√	√	Company's Logo on Screens at Session Halls
√(S1)*	√(S2)*	√(S3)*	Standeers along the Route to Conference Venue
√			Company's Logo on Conference Registration Kit
√(6)	√(4)	√(2)	Complimentary Registrations
√			Evening Session with Student Participants
√	√	√	Banner at Dining Hall
√	√	√	Company provided leaf/ souvenir to be inserted in the Registration kit

\*S1, S2, S3: Sizes

Exhibition Booth Rs 40,000/- per stall	A limited number of exhibit stalls of size 3m x 3m will be available where products can be demonstrated. The stalls will come with a power plug and fascia. The exhibitor is expected to make arrangements for any computers or other equipment that may be needed for the exhibit stall.
Technical Session Sponsorship Rs 25,000/-	A rolling video clip of the company to be playing before the beginning of the formal session in the hall
Company Leaf Rs 15,000/-	Company provided leaf to be inserted in the Registration kit



## BITS PILANI

BITS Pilani is a Deemed to be University that is also an institution of Eminence. BITS was established in the year 1964 as a Deemed to be University by amalgamating pre-existing Birla Colleges of Engineering, Science, Arts, Commerce and Pharmacy. It has been recognized as an Institution of Eminence by Government of India. BITS university features a total of five campuses namely Pilani, Goa, Hyderabad, Dubai and Mumbai. It has been consistently ranked as the top private university in India by various ranking agencies. Electrical & Electronic Engineering departments of BITS campuses offer high quality Undergraduate, Postgraduate and Doctoral Programmes.

BITS-Pilani has been ranked 801-1000 by THE (world-2023) ; ranked 175 by QS (Asia-2023). The Electrical and Electronics Engineering Department has been ranked 301-350 by QS (world-2022) subject ranking.

## CSIR-CEERI PILANI

CSIR-CEERI, Pilani is a National Laboratory in the field of Electronics Engineering under the council of Scientific and Industrial Research, established in the year 1953. It has various research divisions in areas across semiconductor technology, VLSI, electronics systems and Microwave Tubes. CSIR-CEERI, Pilani has a number of firsts to its credit in the field of advanced indigenous electronics technology development. CSIR-CEERI, Pilani has also been the pioneer in the field of semiconductor technology in the country. It has been a resource centre in all the phases of SMDP programme of MeitY/DoE and has also been the programme coordinating institute for SMDP-C2SD.

BITS and CSIR-CEERI, Pilani collaborated to give India its first Masters Degree program in VLSI Design and Microelectronics in the year 1989 and have an active research collaboration.

## PROGRAMME BOOK/SOUVENIR ADVERTISEMENT

Full Page Advertisement in the Programme Book: Rs.15,000/-

Half Page Advertisement in the Programme Book: Rs. 8,000/-

Quarter Page Advertisement in the Programme Book: Rs. 4,000/-

## OTHER CONFERENCE MATERIAL AND REQUIREMENTS

Organisations are also invited to explore with conference convenors directly to propose other conference materials and requirements, such as refreshments, mineral water, T-shirt, souvenirs etc. For these sponsorship items, the organisation's name and logo will feature on the relevant items appropriate places

## DELEGATE SPONSORSHIP

One type of sponsorship would be sponsoring the delegates, from India and abroad, to participate in the conference. The sponsoring organization would sponsor the registration fees of the delegates, their travel expenses, daily subsistence allowance etc. This sponsorship will enable the delegates to participate in the deliberation of the conference and so would be an important contribution towards the success of the conference. This would be an important sponsorship for bringing delegates to attend the conference. The entitlements for delegate sponsorship may be finalized in discussion with the conference chairs of the conference.

## PATRONS

Prof. V Ramgopal Rao  
Vice-Chancellor, BITS Pilani

Prof. Sudhirkumar Barai  
Director, BITS Pilani - Pilani Campus

Dr. P C Panchariya  
Director, CEERI Pilani

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## BANK DETAILS:

Beneficiary Name :	Birla Institute of Technology & Science, Pilani		
IFSC CODE No.	UTIB0001188		
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MICR No.	333211177	Swift Code →	AXISINBB

## CONFERENCE WEBSITE

The information will get updated periodically and will be accessible through the following website:

<https://discovery.bits-pilani.ac.in/VDAT2023/>

Organized by:  
Department of  
Electrical & Electronics Engineering  
BITS Pilani - Pilani Campus

